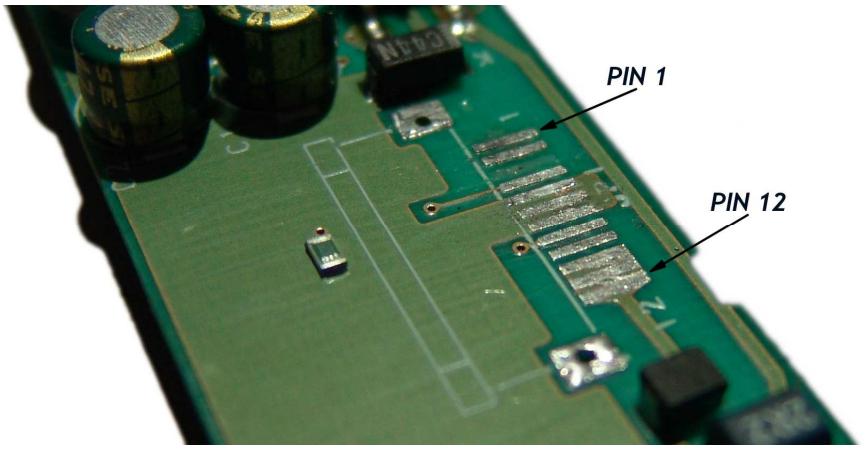
Ascentia 910N inverter

Pinout:

Pin no.	Function
1	*n.c.*
2	*n.c.*
3	*n.c.*
4	BRIGHTNESS
5	GND
6	GND
7	GND
8	ON/OFF
9	*n.c.*
10	12V DC
11	12V DC
12	12V DC

Image:



Inverter board with removed interface connector

Ascentia 910N display

Type: **HITACHI TX26D60VC1CAA**

Details: VGA 640x480, TTL interface, Hirose DF9 31pin header
18bit RGB (6-6-6) + standard DCLK, HSYN, VSYN, DTMG
Running at 5V, ttl inputs are working with a 3V3 logic

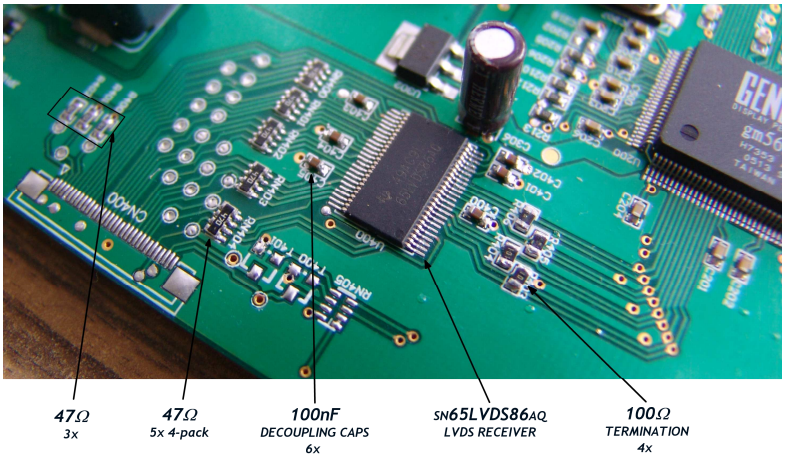
Pinout:

Pin no.	Function
1	GND
2	DCLK
3	HSYN
4	VSYN
5	GND
6	R0
7	R1
8	R2
9	R3
10	R4
11	R5
12	GND
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	GND
20	B0
21	B1
22	B2
23	B3
24	B4
25	B5
26	GND
27	DTMG
28	5V DC
29	5V DC
30	5V DC
31	*n.c.*

Reusing the display

Driver board: **Kontron CRTtoLCD-5**
P/N: 21024

Note: *The 21024 model is a LVDS only output version. While there is a model 21025 which has TTL outputs, we modified the one that was currently available, by soldering a LVDS receiver IC, some resistors and capacitors.*



Connection points: